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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the Patent Application of

Yukio OKUDA

Serial No. 09/964,540

Filed: September 28, 2001

Art Unit: 2858

Examiner: To Be Assigned

For: METHOD AND APPARATUS OF DETERMINING DEFECT-FREE
SEMICONDUCTOR INTEGRATED CIRCUIT

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INFORMATION DISCLOSURE STATEMENT

Commissioner of Patents
Washington, DC 20231

Sir:

The applicant through its representatives and attorneys, hereby brings to the attention of the Examiner the six (6) documents identified on the accompanying Form PTO-1449. Copies are provided.

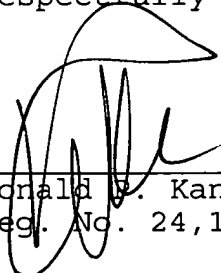
These documents are cited within the description of the above-identified application and cited within the description of the co-pending U.S. Patent Application No. 09/661,793.

This Information Disclosure Statement is being filed prior to mailing of a first office action on the merits in compliance with 37 C.F.R. §1.97(b).

Thus, it is believed that no fees are due. However, if any fees are believed to be due, the Commissioner is authorized to charge any deficit to Deposit Account No. 18-0013.

Applicants respectfully request that the listed documents be considered by the Examiner and be made of record in the present application and that an initialed copy of Form PTO-1449 be returned in accordance with M.P.E.P. § 609.

Respectfully submitted,



Ronald P. Kananen
Reg. No. 24,104

DATE: September 23, 2002

RADER, FISHMAN & GRAUER PLLC
Lion Building
1233 20th Street, N.W.
Washington, D.C. 20036
Tel: (202) 955-3750
Fax: (202) 955-3751
Customer No. 23353